

#5/Election

1-17-03

P. Spruell

DOCKET: BUR920000140US1

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Inventor: Dunn et al.

Date: January 13, 2003

Serial No.: 09/991,142

Examiner: Lee, Eugene

Filing Date: 11/16/01

Art Unit: 2815

Title: SEMICONDUCTOR DEVICE AND METHOD HAVING MULTIPLE  
SUBCOLLECTORS FORMED ON A COMMON WAFER**FAX RECEIVED****RESPONSE TO RESTRICTION REQUIREMENT**

JAN 13 2003

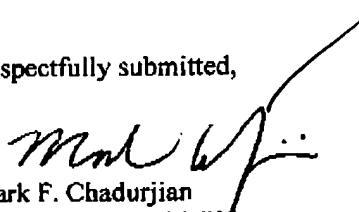
Assistant Commissioner of Patents  
Washington, DC 20231

TECHNOLOGY CENTER 2800

Dear Sir:

Responsive to the Office Action mailed December 13, 2002, and the restriction requirement made therein, Applicants provisionally elect, without traverse, the subject matter of Group I, claims 1-15, for continued examination herein. This election is made without prejudice, and with the understanding that Applicants reserve the right to file further applications based on the unelected claims.

Respectfully submitted,

  
Mark F. Chadurjian  
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Intellectual Property Law - Mail 972E  
1000 River Street  
Essex Junction, VT 05452**CERTIFICATE OF FACSIMILE**

I hereby certify that, on the date shown below, this correspondence is being faxed to the USPTO addressed to: Assistant Commissioner of Patents, Washington, DC 20231  
Attn: William H. Wood, Fax No. 703-872-9318

Date: January 13, 2003Name: Pat Blair

Signature: \_\_\_\_\_

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